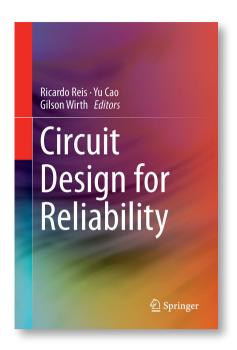


springer.com



2015, X, 282 p. 181 illus., 133 illus. in color.



Hardcover

- ▶ 99,99 € | £90.00 | \$129.00
- ► *106,99 € (D) | 109,99 € (A) | CHF 133.50



Available from your library or

► springer.com/shop



Printed eBook for just

- ▶ € | \$ 24.99
- springer.com/mycopy

R. Reis, Y. Cao, G. Wirth (Eds.)

Circuit Design for Reliability

- ► Provides comprehensive review on various reliability mechanisms at sub-45nm nodes
- Describes practical modeling and characterization techniques for reliability
- Includes thorough presentation of robust design techniques for major VLSI design units
- ► Promotes physical understanding with first-principle simulations

This book presents physical understanding, modeling and simulation, on-chip characterization, layout solutions, and design techniques that are effective to enhance the reliability of various circuit units. The authors provide readers with techniques for state of the art and future technologies, ranging from technology modeling, fault detection and analysis, circuit hardening, and reliability management.

- Provides comprehensive review on various reliability mechanisms at sub-45nm nodes;
- Describes practical modeling and characterization techniques for reliability;
- Includes thorough presentation of robust design techniques for major VLSI design units;
- Promotes physical understanding with first-principle simulations.



Order online at springer.com ► or for the Americas call (toll free) 1-800-SPRINGER ► or email us at: orders-ny@springer.com. ► For outside the Americas call +49 (0) 6221-345-4301 ► or email us at: orders-hd-individuals@springer.com.

The first \in price and the £ and \$ price are net prices, subject to local VAT. Prices indicated with * include VAT for books; the \in (D) includes 7% for Germany, the \in (A) includes 10% for Austria. Prices indicated with ** include VAT for electronic products; 19% for Germany, 20% for Austria. All prices exclusive of carriage charges. Prices and other details are subject to change without notice. All errors and omissions excepted.